

FIG. I.

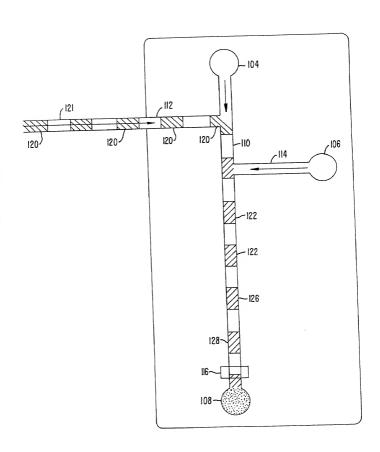


FIG. 2A.

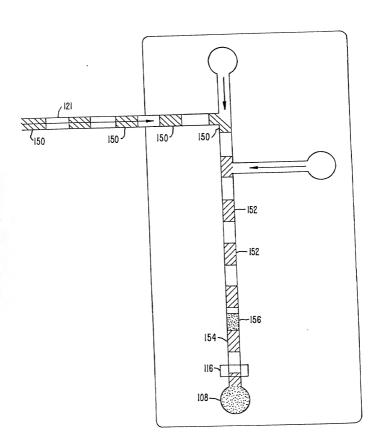


FIG. 2B.

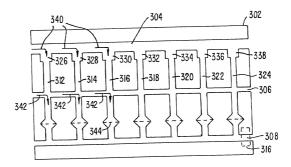


FIG. 3.

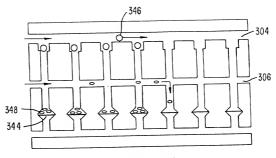


FIG. 4A.

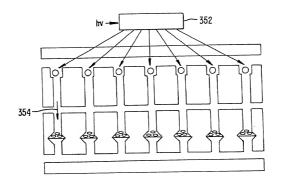
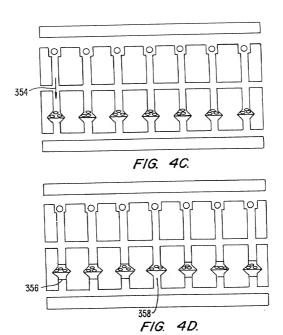
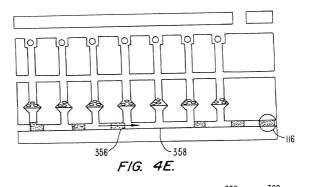


FIG. 4B.





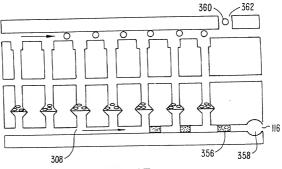


FIG. 4F.

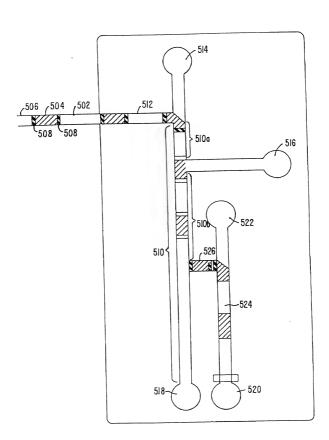
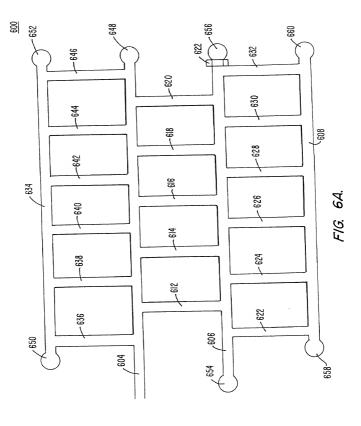
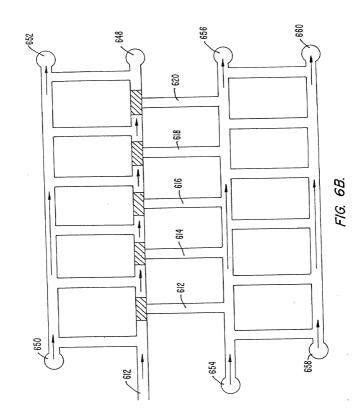
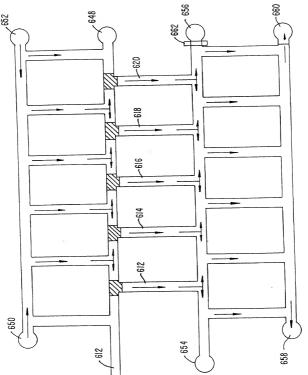


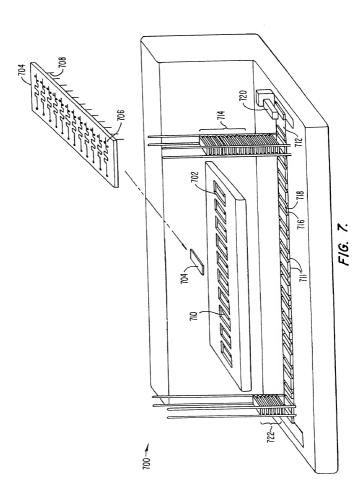
FIG. 5.







F1G. 6C.



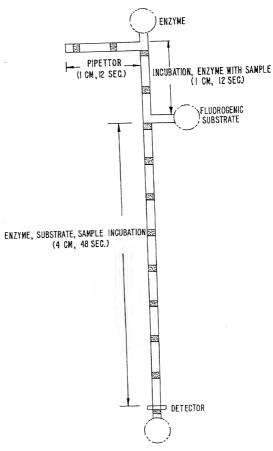


FIG. 8



SAMPLE ADDISITION TIMING DIAGRAM

